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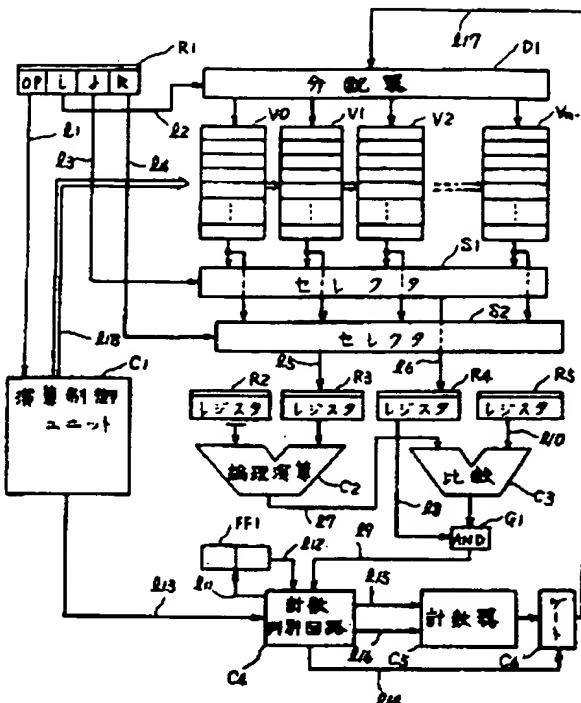
APPLICATION DATE : 25-05-84
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APPLICANT : HITACHI LTD;

INVENTOR : INAGAMI YASUHIRO;

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TITLE : VECTOR PROCESSOR



ABSTRACT : PURPOSE: To count the number of extracted vector elements that conform to a specified pattern continuously by generating the number of continuous specific patterns in a part masked by a mask register to resultant vector.

CONSTITUTION: When a vector element of input vector is inputted to a register R3, logical operation is made with a mask pattern stored in a register R2 by a logical operator G2. The result is compared with a specific pattern stored in a register R5 by a comparator C3, and the result is sent to a count discrimination circuit C4 through an AND gate G1. When specific patterns are applied continuously from a line L9, the count discrimination circuit C4 applies count pulse to a counter C5 through a line L15 under control of a flip-flop FF1. The result of counting is stored in a vector register by a distributor D1 from a gate C6 through a line L17.

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